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(VLSI.320PA)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

jc835 U.S. PTO
09/997609

11/29/01

11/29/01
J1054 U.S. PTO

Applicant: Payne et al.
Docket: US018186
Title: HIGH-SPEED INTERCHIP INTERFACE PROTOCOL

CERTIFICATE UNDER 37 CFR 1.10

Express Mail' mailing label number: EL 735699896 US

Date of Deposit: November 29, 2001

I hereby certify that this paper or fee is being deposited with the United States Postal Service 'Express Mail Post Office To Addressee' service under 37 CFR 1.10 and is addressed to the Assistant Commissioner for Patents, Arlington, VA 22202

By: Kelly S. Wattigney
Name: Kelly S. Wattigney

BOX PATENT APPLICATION
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Sir:

We are transmitting herewith the attached:

- ☒ Transmittal sheet containing Certificate under 37 CFR 1.10.
- ☒ Patent Application: Pages Numbered 1-27, including 1 page of Abstract; and 15 claims
- ☒ 6 sheets of informal drawings
- ☒ An executed Assignment and Declaration
- ☒ Assignment of the invention to Koninklijke Philips Electronics N.V. Recordation Form Cover Sheet
- ☒ Enclosed find two checks in the amounts of \$740.00 in payment of the filing fee and \$40.00 in payment of the assignment recordation.
- ☒ If necessary, charge/credit Deposit Account No. 50-0996 (VLSI.320PA) for any deficiencies/overpayments.
- ☒ 2 Return Postcards

The fees are calculated as follows:

Assignment Recordation Fee = \$40.00

Basic Filing Fee = \$740.00

Fee for total number of claims in excess of 20 = $\$18 * (15-20) = 0.00$

Fee for total number of independent claims in excess of 3 = $\$84 * (3 - 3) = 0.00$

Total filing fee due = \$740.00

Direct all correspondence to the address below:

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